



Chapter 8: Finite Automata with Output

Two equivalent theoretical machines with output are introduced.



Chapter 8: Finite Automata with Output

Finite automata as mathematical models of computers

letters of words	↔	input
transitions	↔	execution of instructions (a program)
state	↔	contents of memory
?	↔	output



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- A **Moore machine** is a collection of 5 things:
 1. a finite set of states $q_0, q_1, q_2, \dots$, where q_0 is designated the start state
 2. an alphabet Σ of input letters
 3. an alphabet Γ of output characters
 4. a transition table that shows for each state and each input letter what state to go to next.
 5. an output table that shows what character is output (or printed) when entering a state.

(state, letter from Σ) $\xrightarrow{\text{transition}}$ state

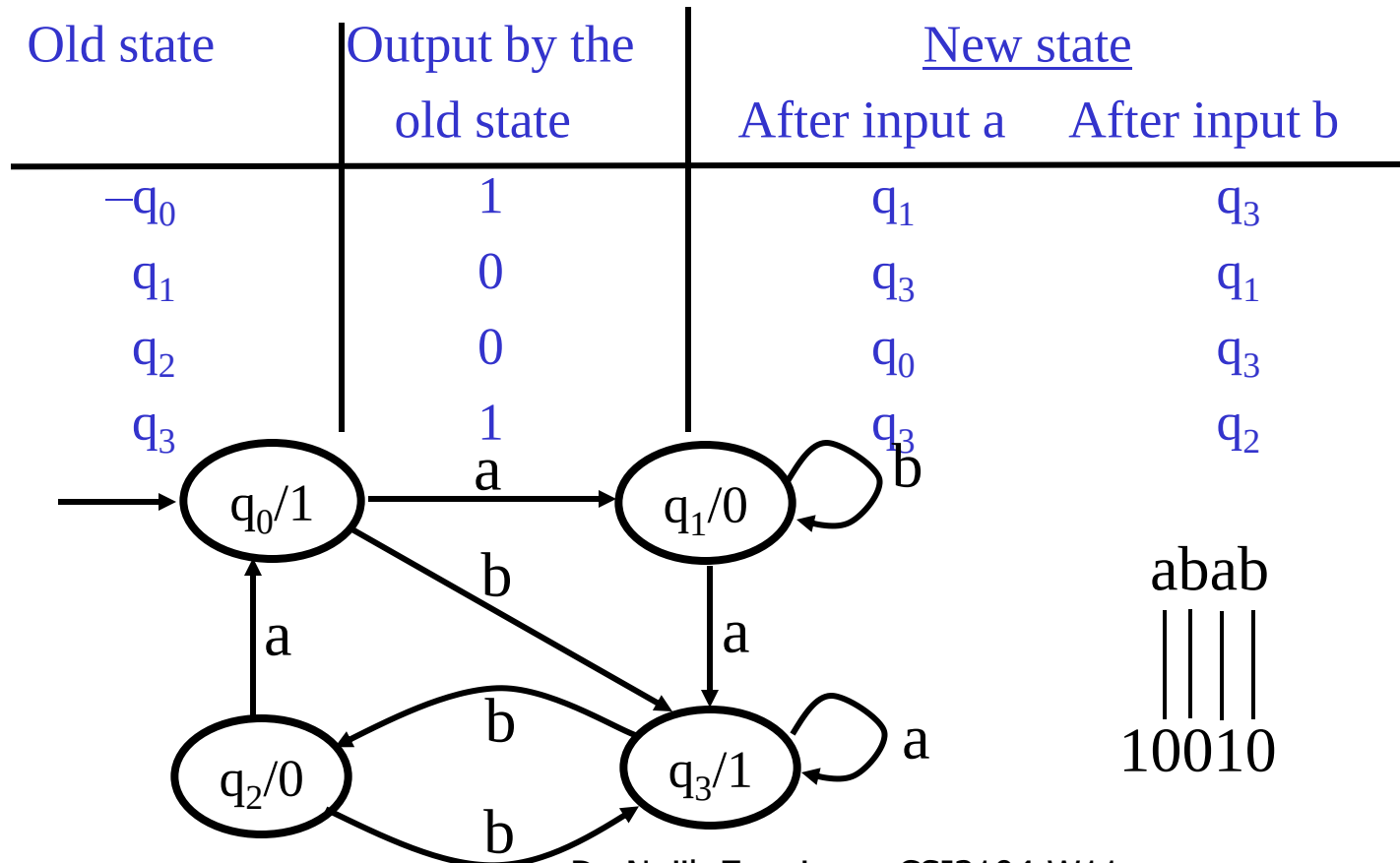


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Example: states = $\{q_0, q_1, q_2, q_3\}$

$\Sigma = \{a, b\}$

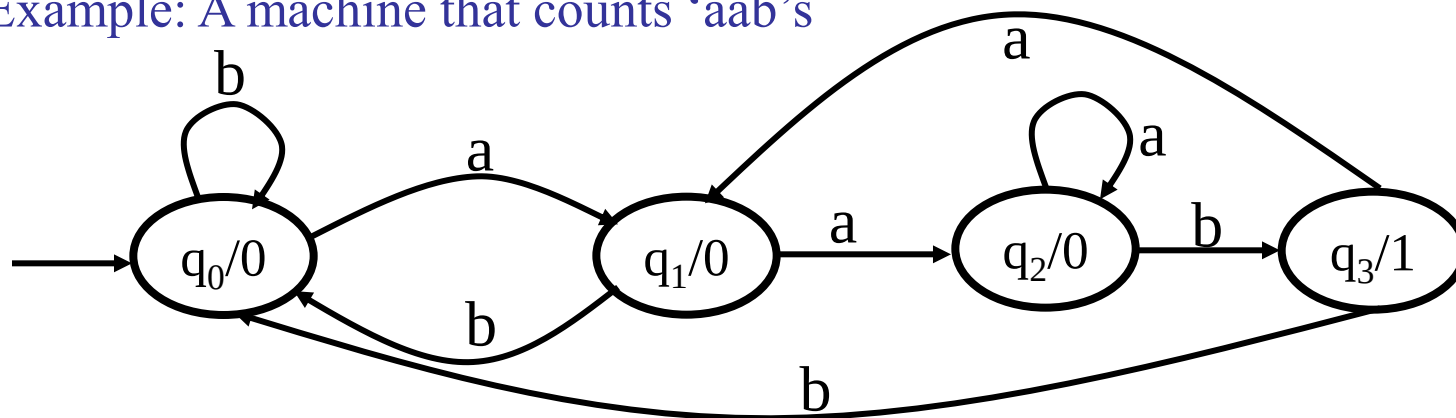
$\Gamma = \{0, 1\}$





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Example: A machine that counts 'aab's



Input		a	a	a	b	a	b	b	a	a	b	b
State	q ₀	q ₁	q ₂	q ₂	q ₃	q ₁	q ₀	q ₀	q ₁	q ₂	q ₃	q ₀
Output	0	0	0	0	1	0	0	0	0	0	1	0

Printing 1 $\approx$ final state

words that end in aab



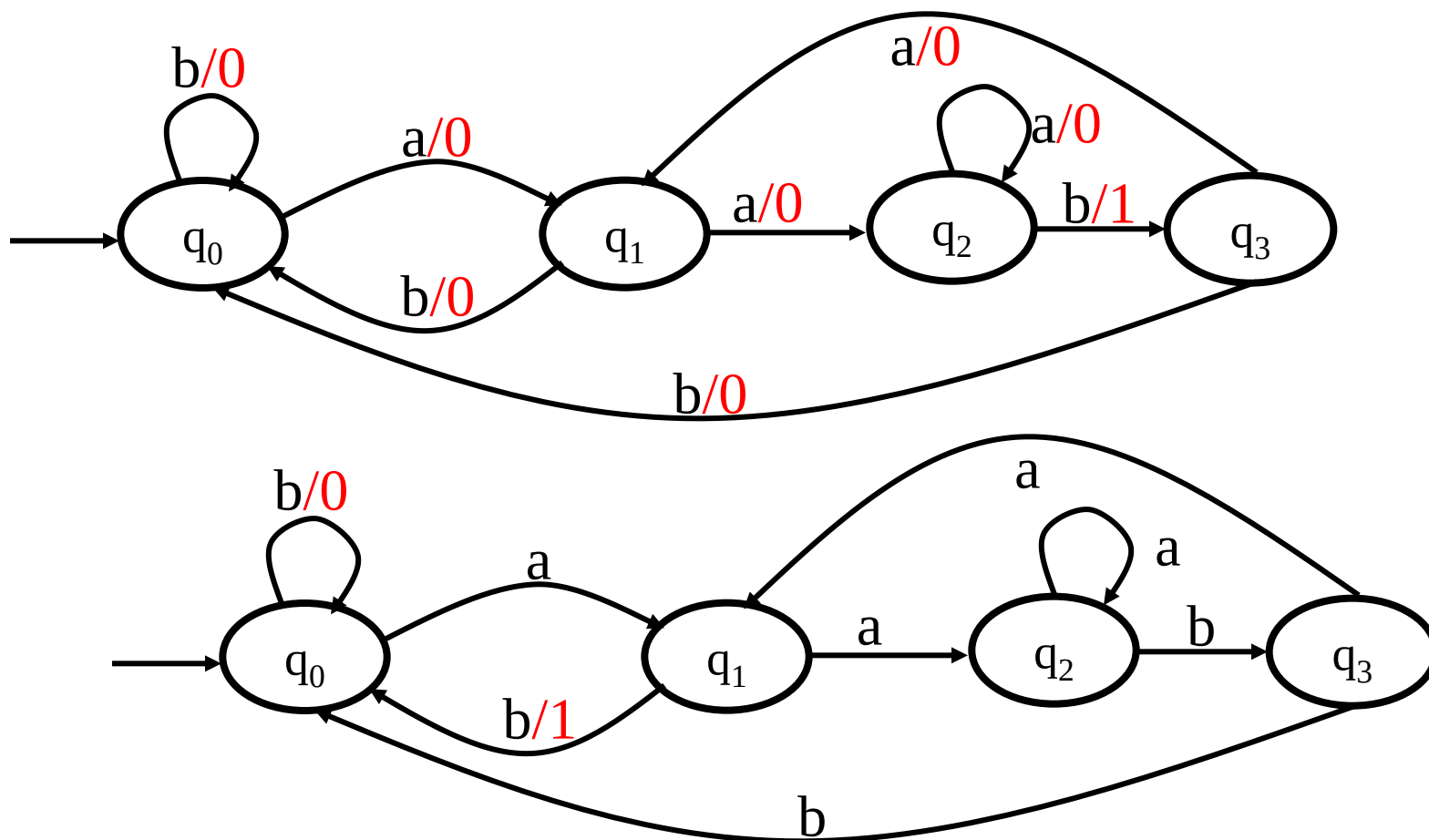
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- A **Mealy machine** is:
 1. a finite set of states $q_0, q_1, q_2, \dots$, where q_0 is designated the start state
 2. an alphabet Σ of input letters
 3. an alphabet Γ of output characters
 4. a finite set of transitions that indicate, for each state and letter of the input alphabet, the state to go to next and the character that is output (printed).





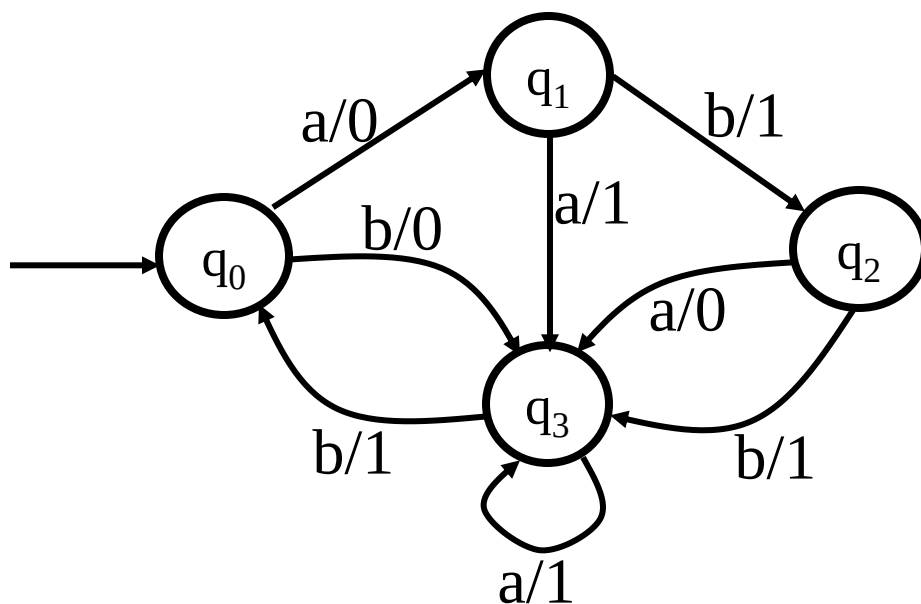
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Example

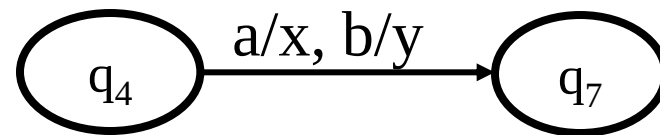
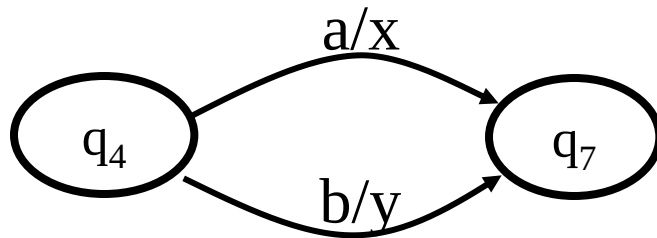


baaba
| | | | |
01110



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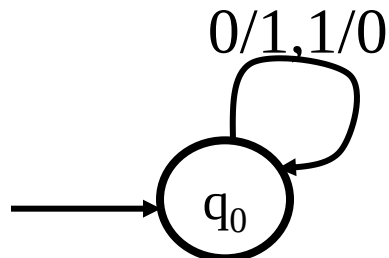
Abbreviation:



Example: binary complement

$\Sigma = \{0, 1\}$

$\Gamma = \{0, 1\}$

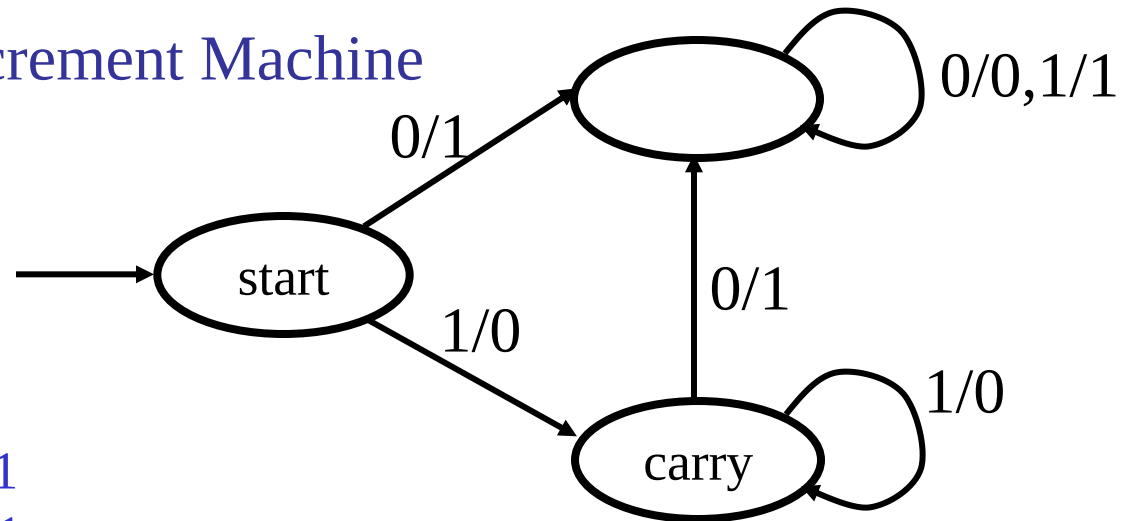


101	001010
010	110101



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Example: an Increment Machine



decimal: 11
binary : 1011
reverse: 1101

|||||
00 11

reverse: 1100
decimal: 12

1111
↓
0000 (overflow)

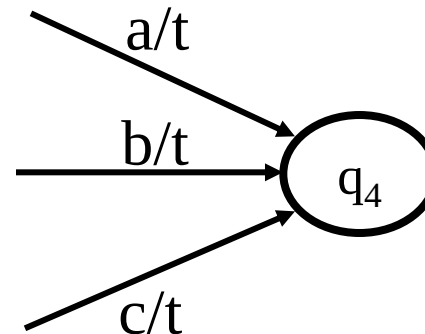
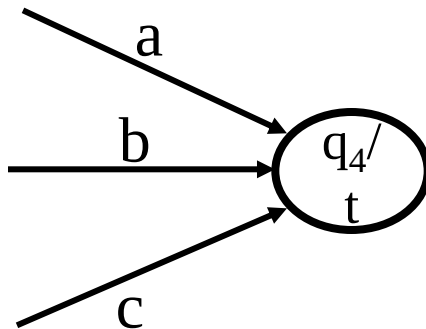


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Definition: Let M_0 be a Moore machine that prints x in the start state. Let M_e be a Mealy machine. The two machines are **equivalent** if for every input, whenever the output from M_e is w , the output from M_0 is xw .

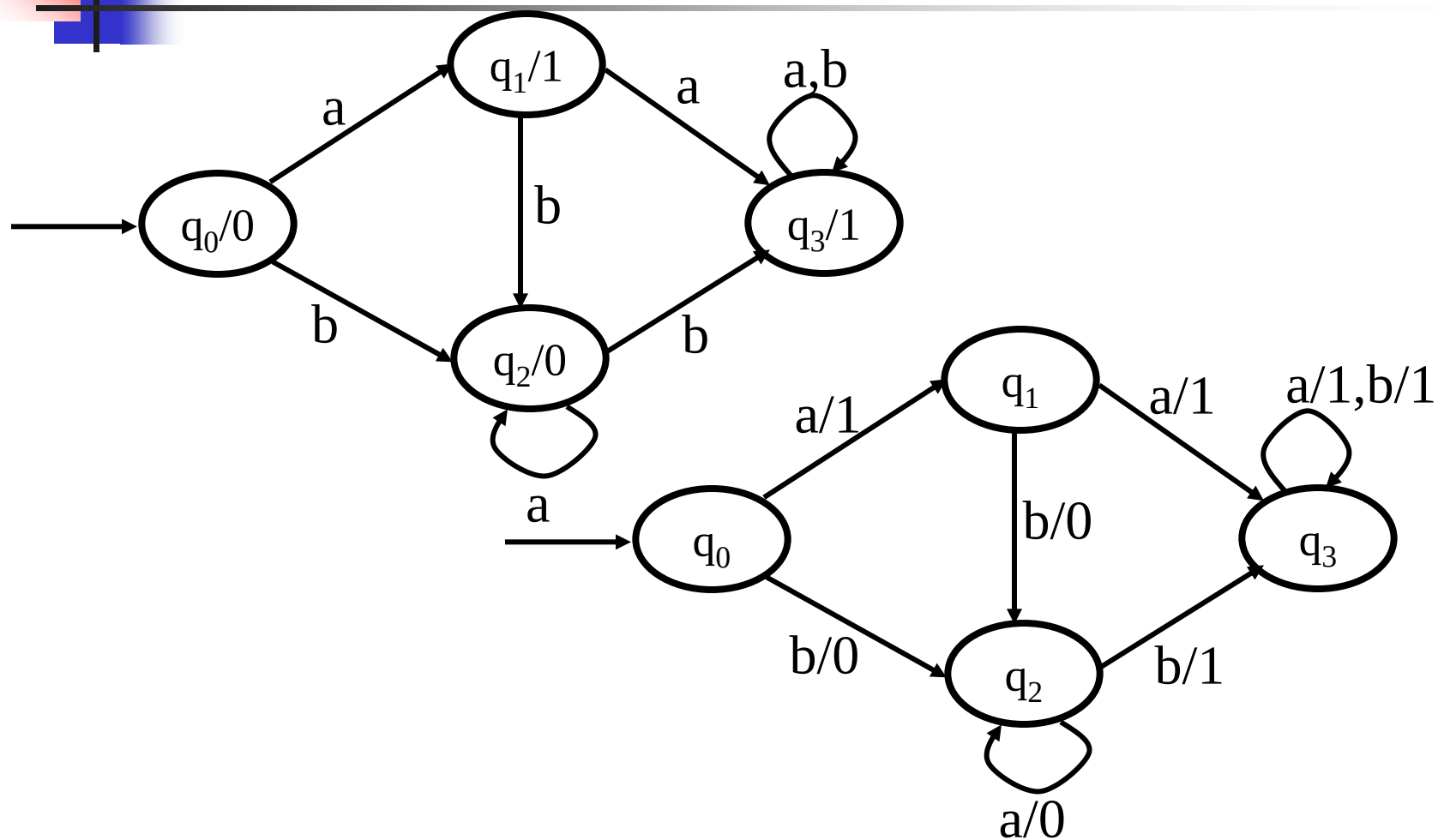
Theorem: For every Moore machine, there exists a Mealy machine that is equivalent to it.

Proof: By constructive algorithm





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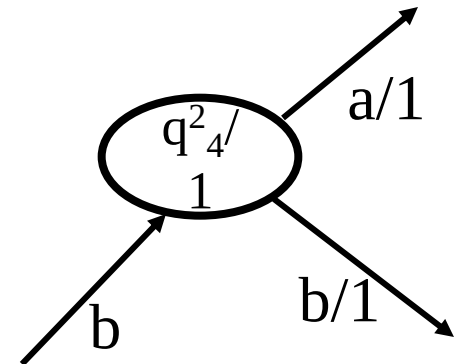
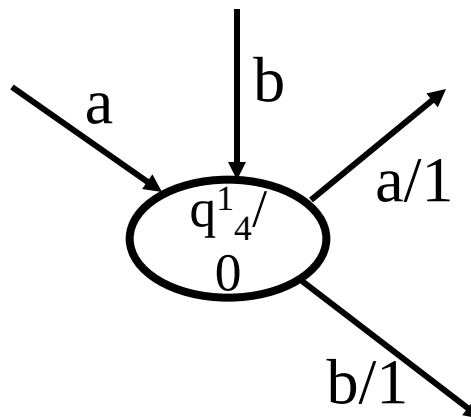
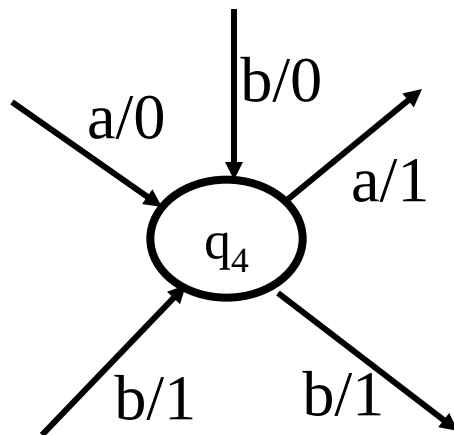
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Theorem: For every Mealy machine, there exists a Moore machine that is equivalent to it.

Proof: By constructive algorithm

One copy of the state for each letter in Γ that labels an arrow entering it.

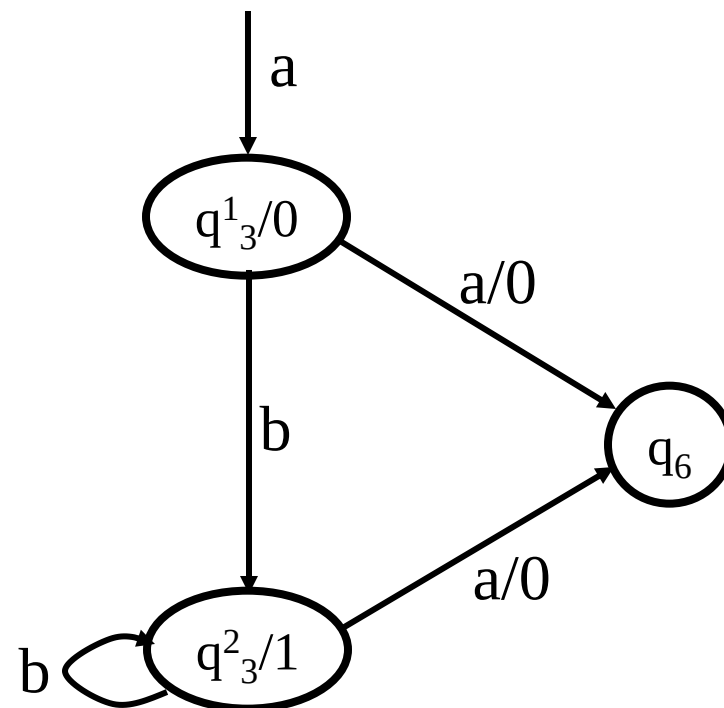
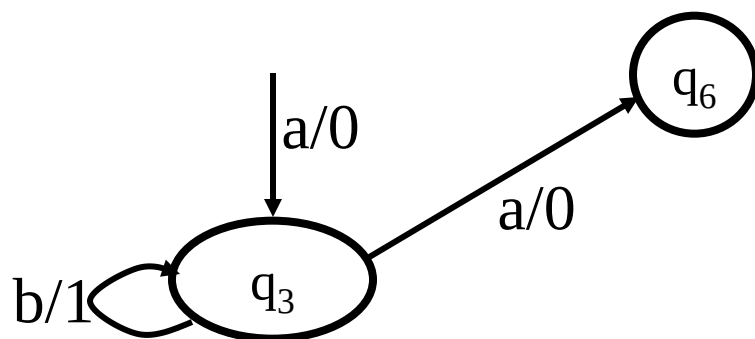




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We must consider

1. incoming edges
2. outgoing edges
3. loops

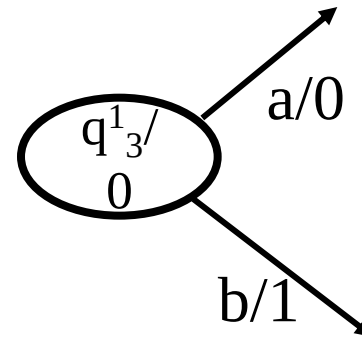
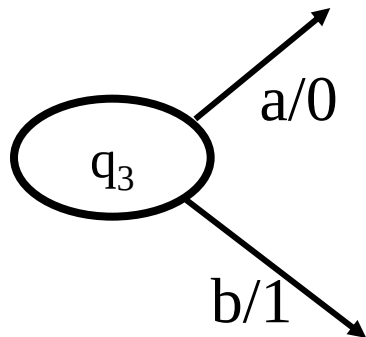




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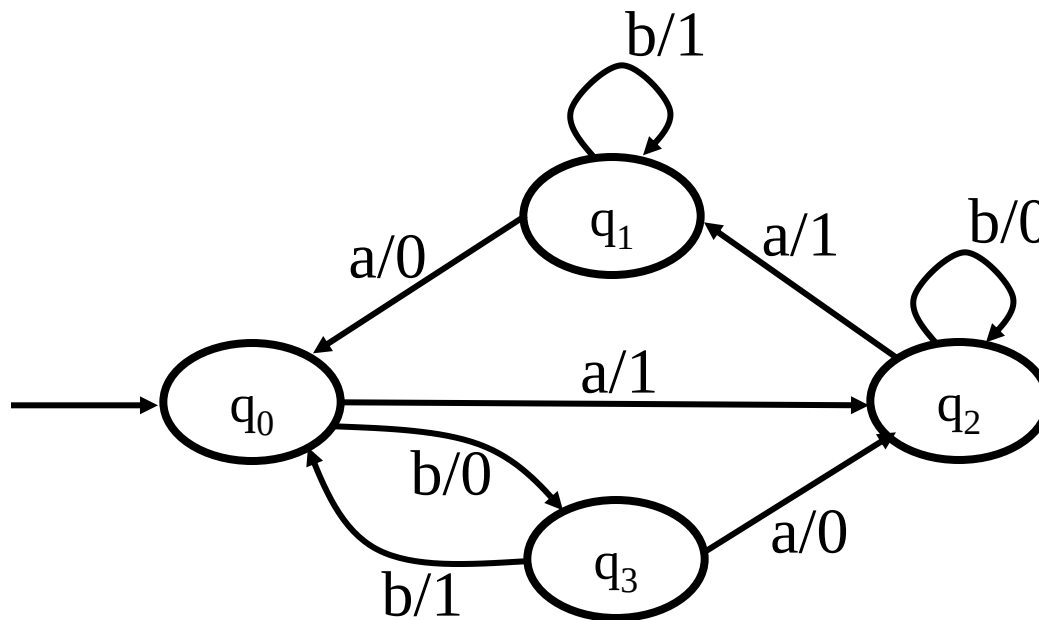
If there is no entering arrow, choose any letter from Γ .



Choose any copy of q_0 as the start state.

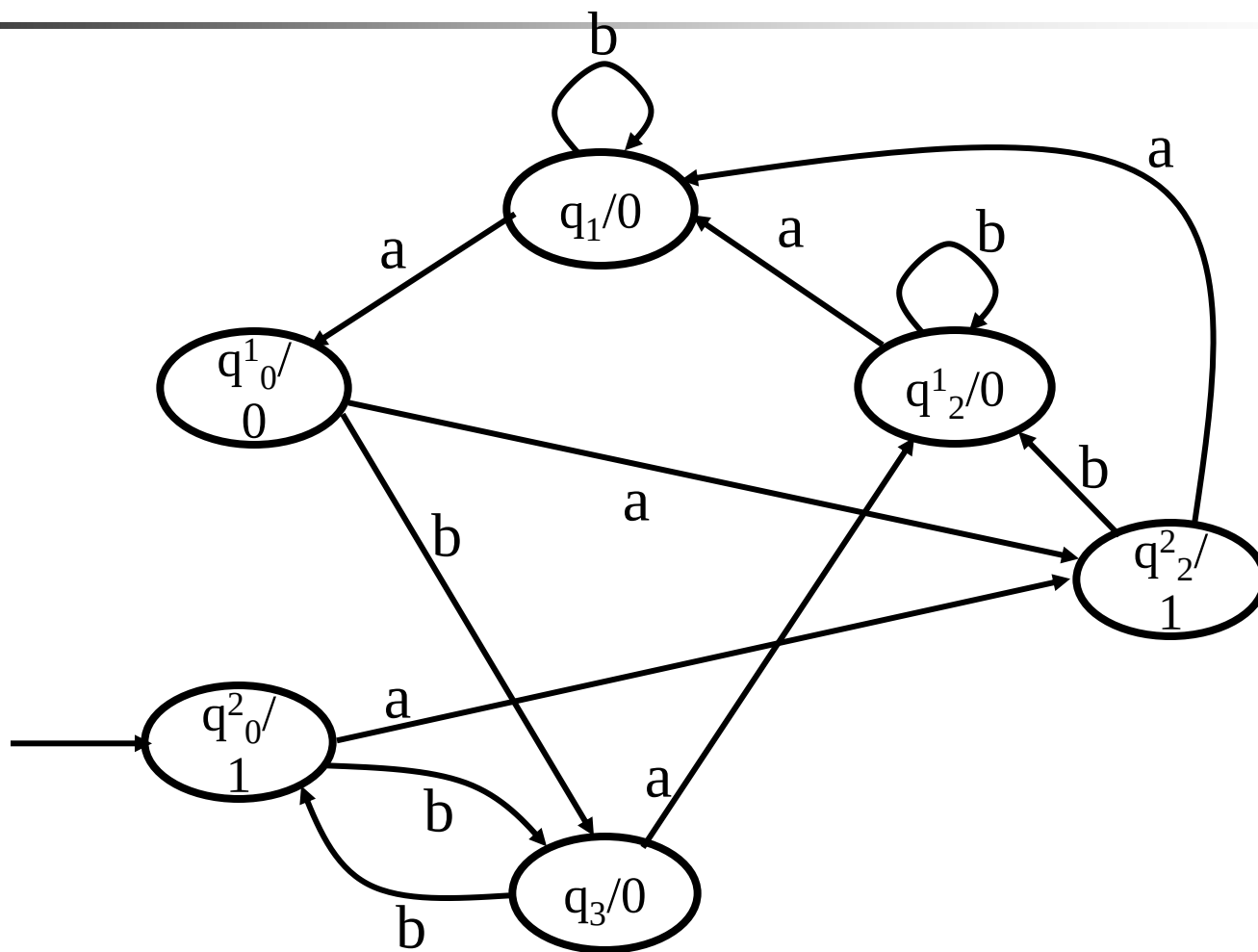


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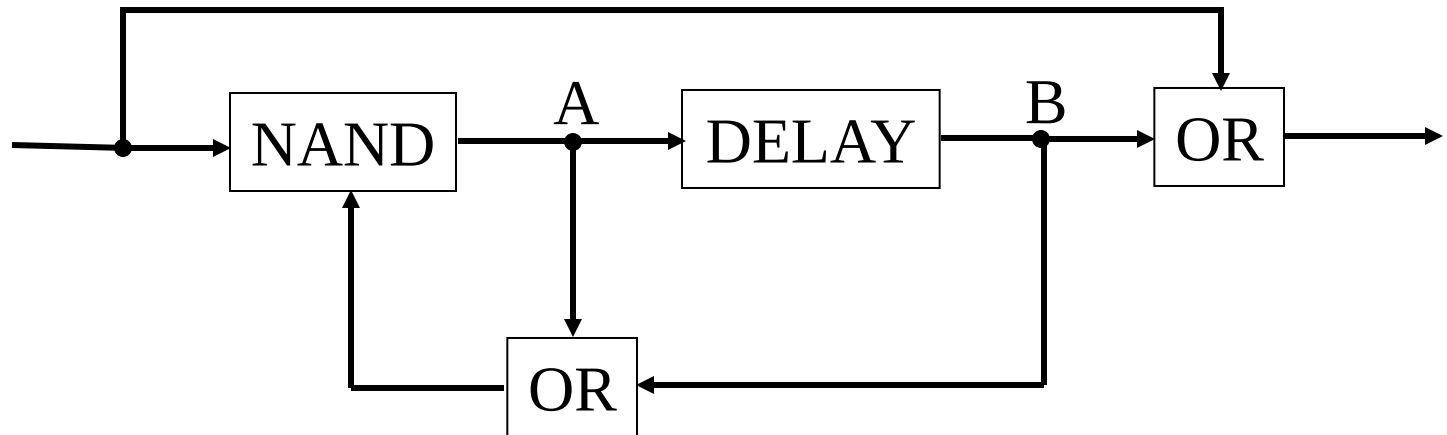
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Example: Mealy Machine Describing a Circuit



q_0 : $A=0, B=0$ q_1 : $A=0, B=1$ q_2 : $A=1, B=0$ q_3 : $A=1, B=1$

Transitions:

New B = old A

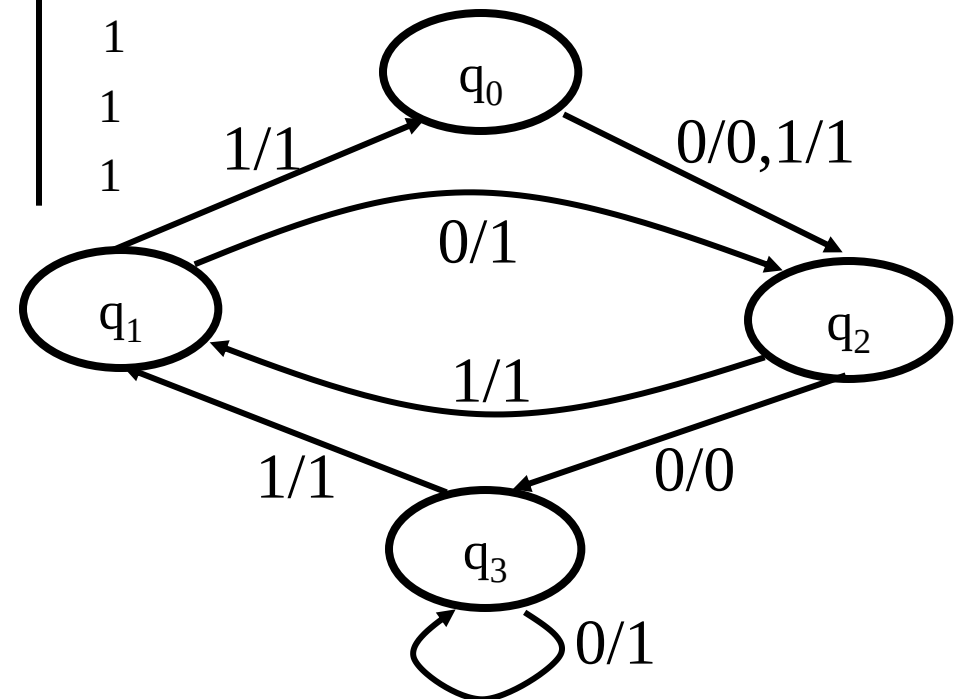
New A = input NAND (old A OR old B)

Output = input OR (old B)



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Old state	<u>Input 0</u>		<u>Input 1</u>	
	new state	output	new state	output
q_0	q_2	0	q_2	1
q_1	q_2	1	q_0	1
q_2	q_3	0	q_1	1
q_3	q_3	1	q_1	1





Chapter 8: Finite Automata with Output Automata Summary



	Finite Automata	Tr. Graphs	Generalized Tr. Graphs	non-determ automata	Moore Machines	Mealy Machines
start states	1	≥ 1	≥ 1	1	1	1
final states	≥ 0	≥ 0	≥ 0	≥ 0	0	0
labels on arrows	letters of Σ	words of Σ^*	Reg. Expr. over Σ	letters of Σ	letters of Σ	i/o, $i \in \Sigma$, $o \in \Gamma$
# of trans. from each state	1 transition for each letter of Σ	≥ 0	≥ 0	≥ 0	1 transition for each letter of Σ	1 transition for each letter of Σ
Deterministic?	Yes	No	No	No	Yes	Yes
Output?	No	No	No	No	Yes	Yes